## **Refine Search**

## Search Results -

Terms	Documents
(substrate and (flip adj chip) and capacitor and polysilane).clms.	0

US Pre-Grant Publication Full-Text Database

**US Patents Full-Text Database** 

US OCR Full-Text Database

**EPO Abstracts Database** JPO Abstracts Database

**Derwent World Patents Index** 

IBM Technical Disclosure Bulletins

Search:

Database:

L7			•	Refine Search
	Recall Text	Clear		 Interrupt

Clear

## **Search History**

DATE: Thursday, October 27, 2005 Printable Copy Create Case

Set Name Query	Hit Count	<u>Set Name</u>
side by side		result set
DB=PGPB; PLUR=YES; OP=ADJ		
<u>L7</u> (substrate and (flip adj chip) and capacitor and polysilane).clms.	0	<u>L7</u>
DB=JPAB; PLUR=YES; OP=ADJ		
<u>L6</u> substrate and (flip adj chip) and capacitor and polysilane	0	<u>L6</u>
DB=EPAB; PLUR=YES; OP=ADJ		
L5 substrate and (flip adj chip) and capacitor and polysilane	0	<u>L5</u>
DB=USPT; PLUR=YES; OP=ADJ		
<u>L4</u> substrate and (flip adj chip) and capacitor and polysilane	9	<u>L4</u>
DB=PGPB; PLUR=YES; OP=ADJ		
<u>L3</u> substrate and (flip adj chip) and capacitor and polysilane	5	<u>L3</u>
DB=TDBD; PLUR=YES; OP=ADJ		,
<u>L2</u> substrate and (flip adj chip) and capacitor and polysilane	0	<u>L2</u>
DB=DWPI; PLUR=YES; OP=ADJ		
<u>L1</u> substrate and (flip adj chip) and capacitor and polysilane	0	<u>L1</u>

**END OF SEARCH HISTORY**